

Fig. 1

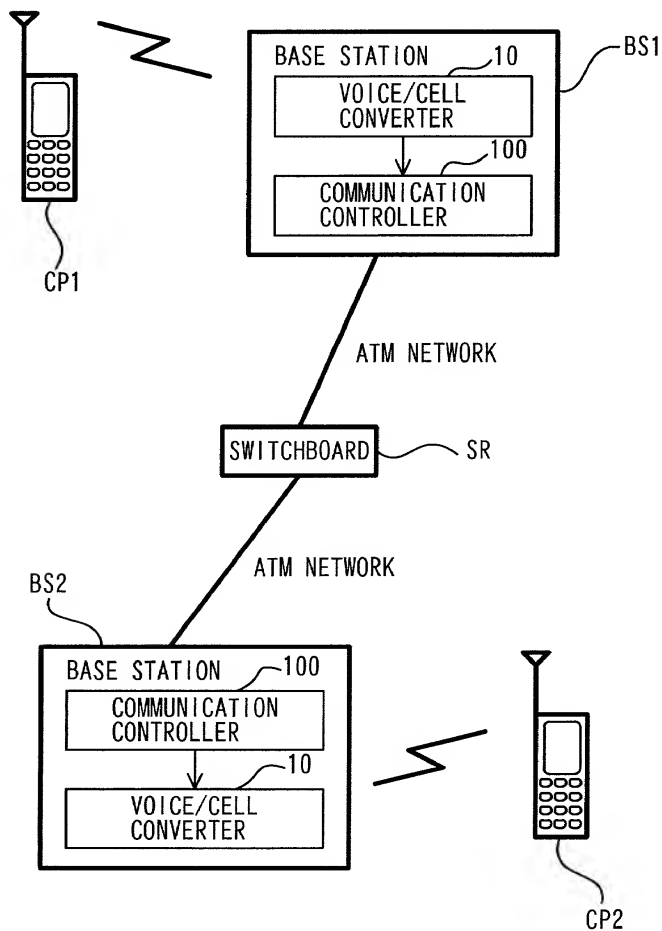


Fig. 2

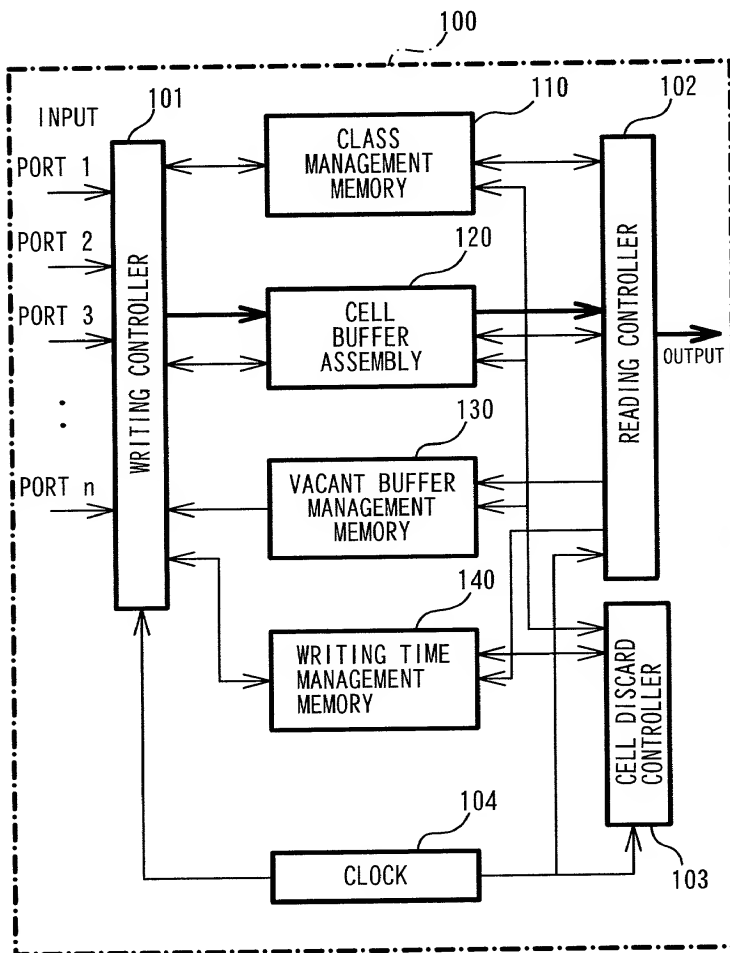


Fig. 3

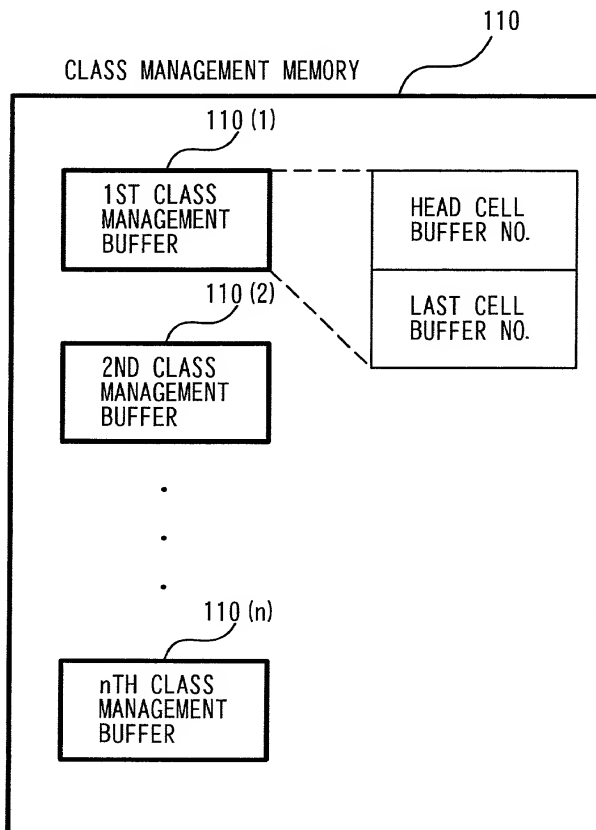


Fig. 4

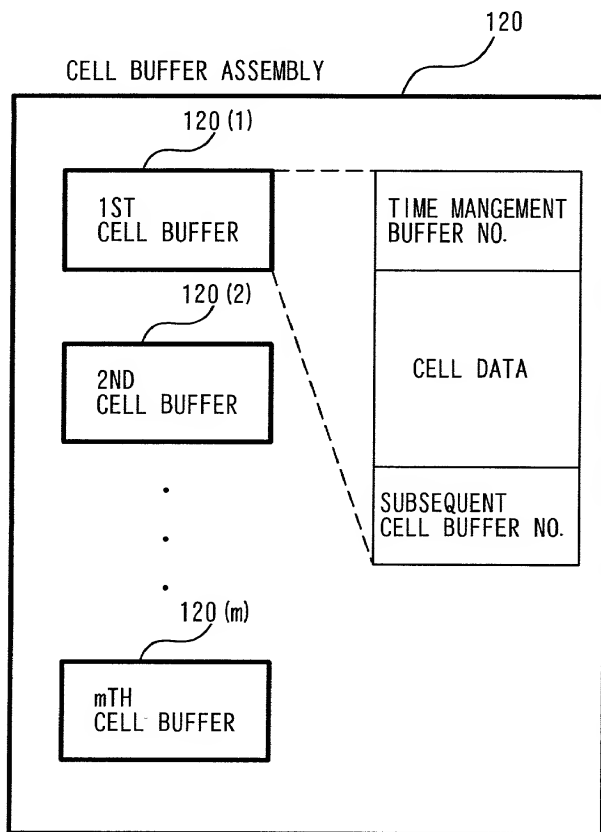


Fig. 5

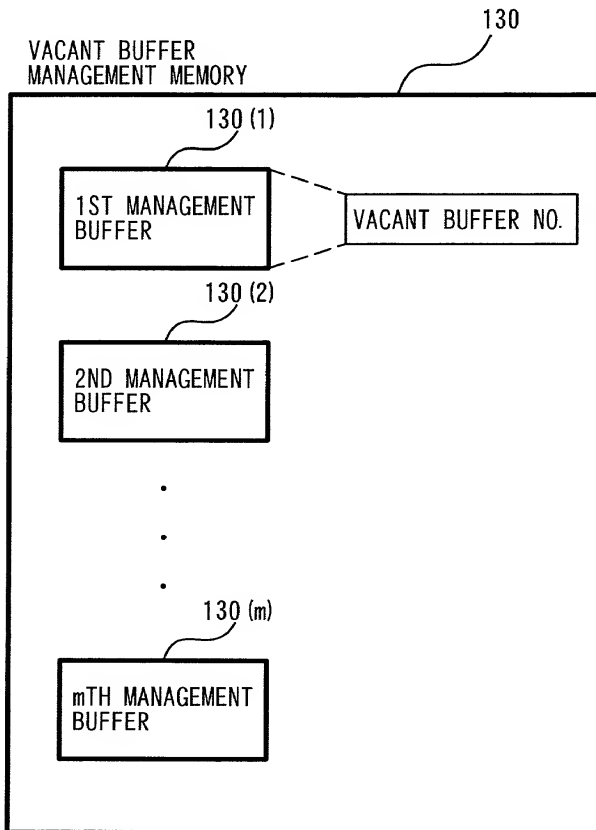


Fig. 6

WRITING TIME MANAGEMENT MEMORY

140

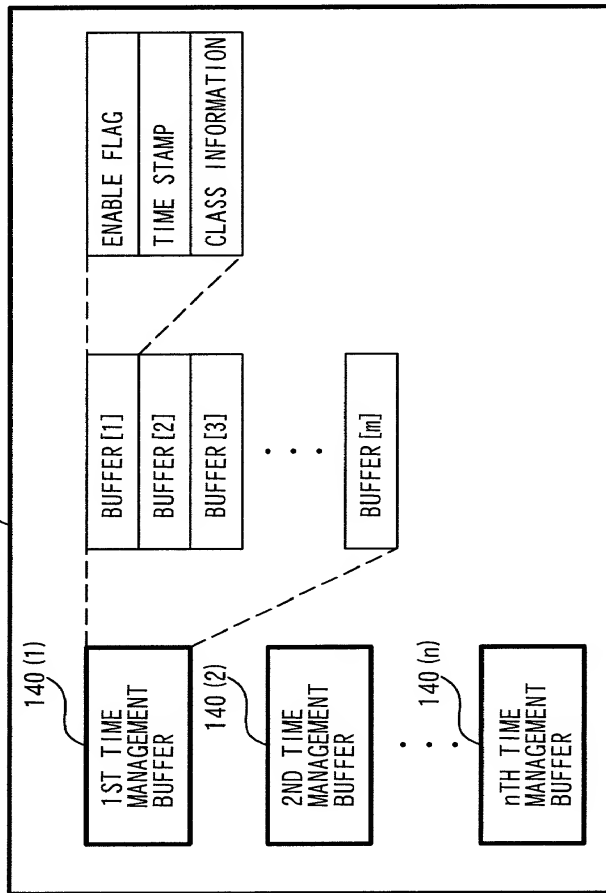


Fig. 7

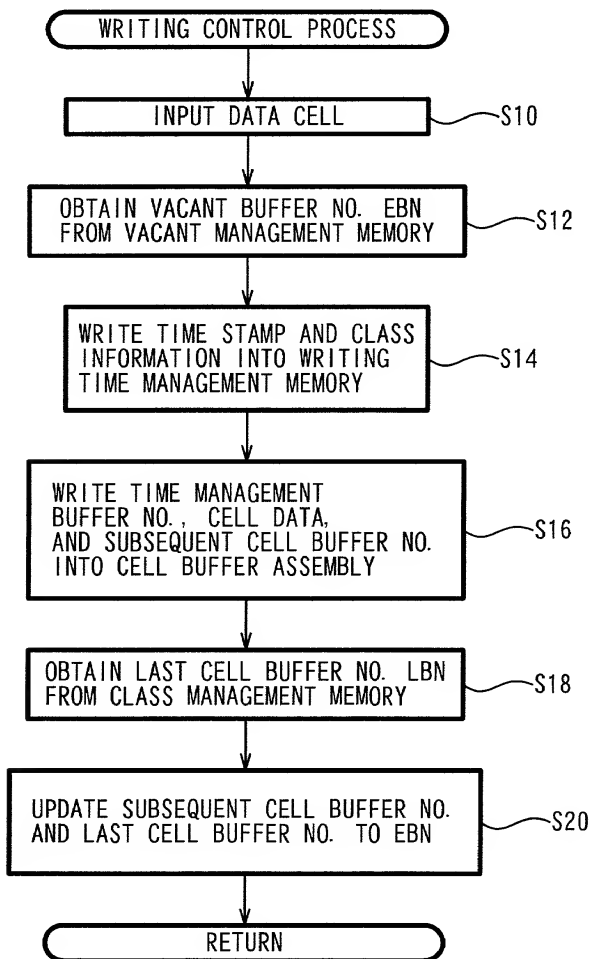


Fig. 8

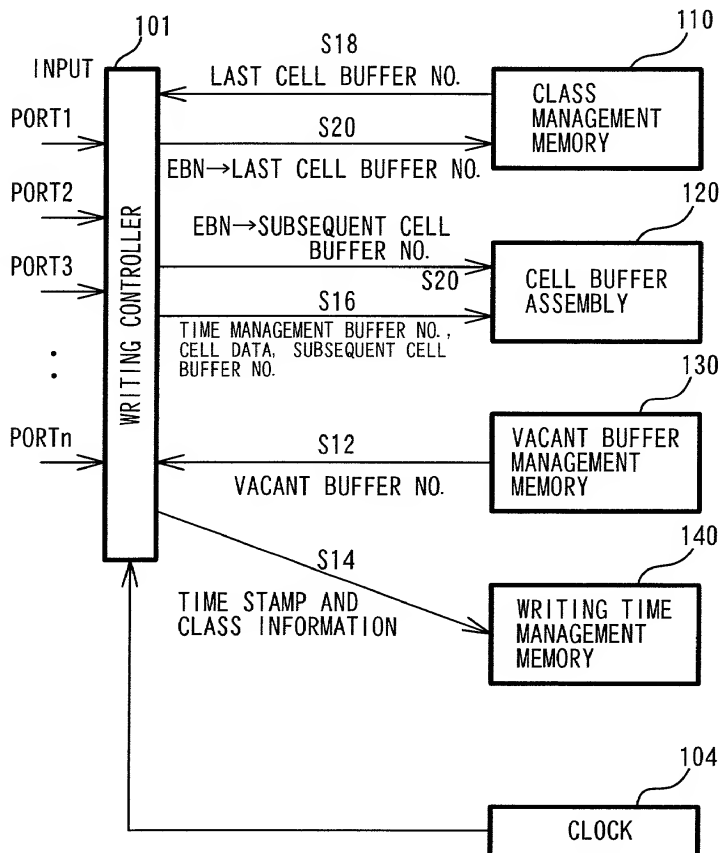


Fig.9

			Initial	S14	S16	S20
1ST Class Management Buffer	Head cell buffer No.		1	1	1	1
	Last cell buffer No.		1	1	1	3
1ST Cell Buffer	Time management buffer No.		1	1	1	1
	Cell data		AAA	AAA	AAA	AAA
	Subsequent cell buffer No.		1	1	1	3
2ND Cell Buffer	Time management buffer No.					
	Cell data					
	Subsequent cell buffer No.					
3rd Cell Buffer	Time management buffer No.				2	2
	Cell data				BBB	BBB
	Subsequent cell buffer No.				3	3
4th Cell Buffer	Time management buffer No.					
	Cell data					
	Subsequent cell buffer No.					
Vacant Buffer Management Memory	1ST management buffer		3			
	2ND management buffer		4	4	4	4
	3rd management buffer		2	2	2	2
	4th management buffer					
1ST Time Management Buffer	Buffer[1]	Enable flag	1	1	1	1
		Time stamp	aa:aa	aa:aa	aa:aa	aa:aa
		Class information	1	1	1	1
	Buffer[2]	Enable flag	0	1	1	1
		Time stamp		bb:bb	bb:bb	bb:bb
		Class information		1	1	1
	Buffer[3]	Enable flag	0	0	0	0
		Time stamp				
		Class information				
	Buffer[4]	Enable flag	0	0	0	0
		Time stamp				
		Class information				

Fig. 10

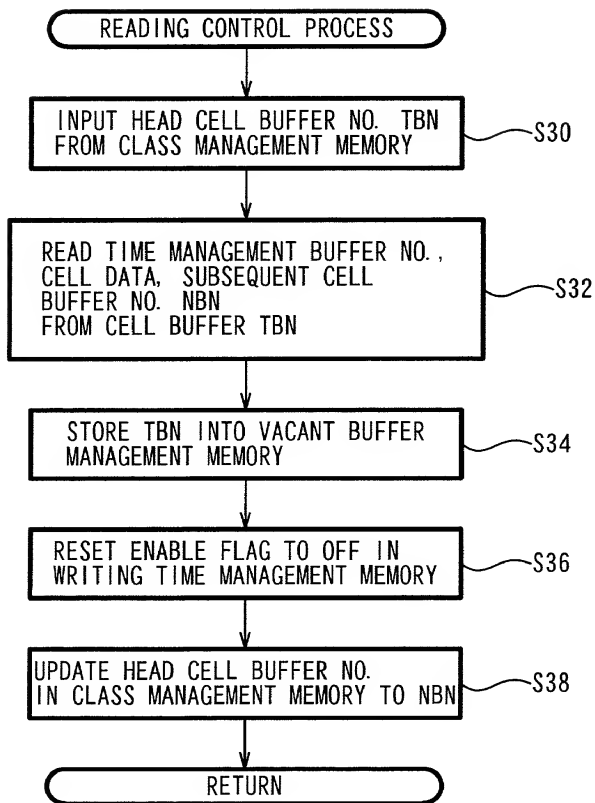


Fig. 11

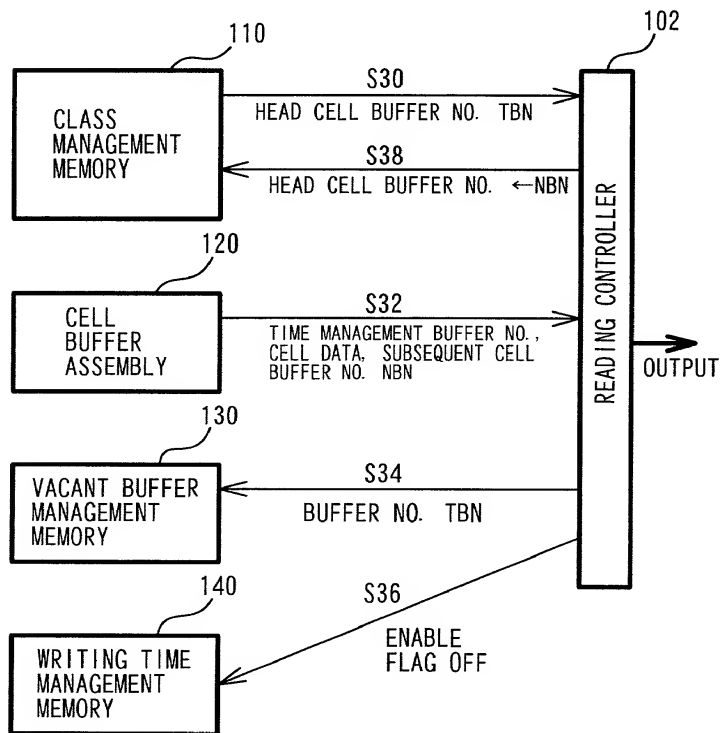


Fig.12

			Initial	S32	S34	S36	S38
1ST Class Management Buffer	Head cell buffer No.		1	1	1	1	3
	Last cell buffer No.		3	3	3	3	3
1ST Cell Buffer	Time management buffer No.		1				
	Cell data		AAA				
	Subsequent cell buffer No.		3				
2ND Cell Buffer	Time management buffer No.						
	Cell data						
	Subsequent cell buffer No.						
3rd Cell Buffer	Time management buffer No.		2	2	2	2	2
	Cell data		BBB	BBB	BBB	BBB	BBB
	Subsequent cell buffer No.		3	3	3	3	3
4th Cell Buffer	Time management buffer No.						
	Cell data						
	Subsequent cell buffer No.						
Vacant Buffer Management Memory	1ST management buffer						
	2ND management buffer		4	4	4	4	4
	3rd management buffer		2	2	2	2	2
	4th management buffer				1	1	1
1ST Time Management Buffer	Buffer[1]	Enable flag	1	1	1	0	0
		Time stamp	aa:aa	aa:aa	aa:aa		
		Class information	1	1	1		
	Buffer[2]	Enable flag	1	1	1	1	1
		Time stamp	bb:bb	bb:bb	bb:bb	bb:bb	bb:bb
		Class information	1	1	1	1	1
	Buffer[3]	Enable flag	0	0	0	0	0
		Time stamp					
		Class information					
	Buffer[4]	Enable flag	0	0	0	0	0
		Time stamp					
		Class information					

Fig. 13

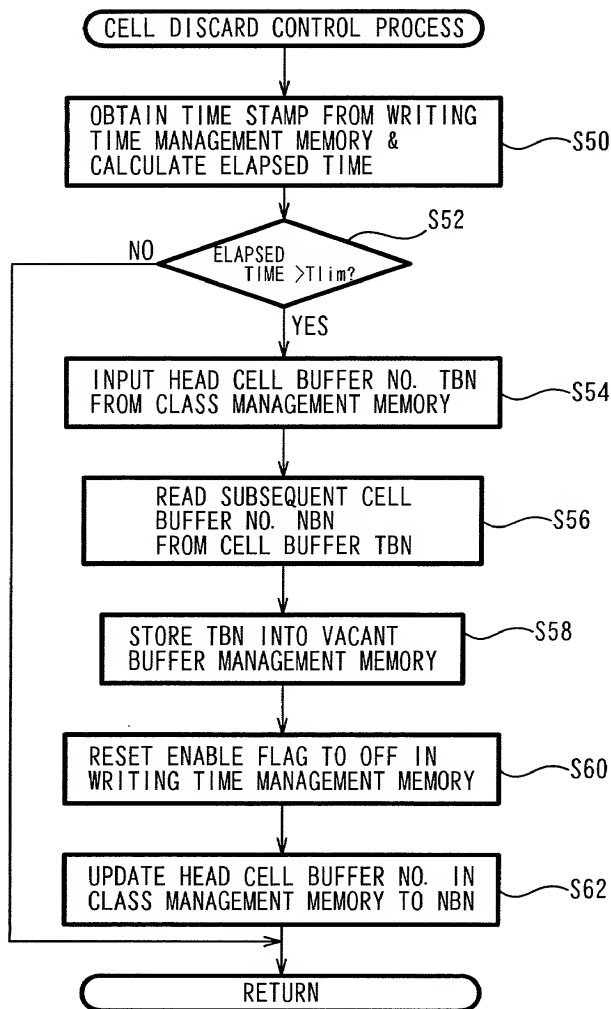
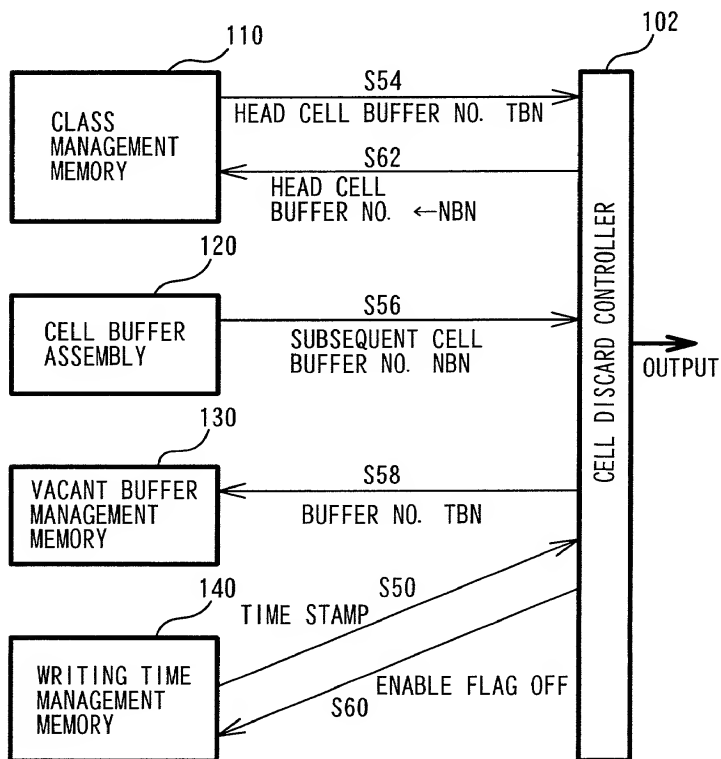


Fig. 14



		Initial	S56	S58	S60	S62	
1ST Class Management Buffer	Head cell buffer No.	1	1	1	1	3	
	Last cell buffer No.	3	3	3	3	3	
1ST Cell Buffer	Time management buffer No.	1	1	1	1		
	Cell data	AAA	AAA	AAA	AAA		
	Subsequent cell buffer No.	3	3	3	3		
2ND Cell Buffer	Time management buffer No.						
	Cell data						
	Subsequent cell buffer No.						
3rd Cell Buffer	Time management buffer No.	2	2	2	2	2	
	Cell data	BBB	BBB	BBB	BBB	BBB	
	Subsequent cell buffer No.	3	3	3	3	3	
4th Cell Buffer	Time management buffer No.						
	Cell data						
	Subsequent cell buffer No.						
Vacant Buffer Management Memory	1ST management buffer						
	2ND management buffer	4	4	4	4	4	
	3rd management buffer	2	2	2	2	2	
	4th management buffer			1	1	1	
1ST Time Management Buffer	Buffer[1]	Enable flag	1	1	1	0	0
		Time stamp	aa:aa	aa:aa	aa:aa		
		Class information	1	1	1		
	Buffer[2]	Enable flag	1	1	1	1	1
		Time stamp	bb:bb	bb:bb	bb:bb	bb:bb	bb:bb
		Class information	1	1	1	1	1
	Buffer[3]	Enable flag	0	0	0	0	0
		Time stamp					
		Class information					
	Buffer[4]	Enable flag	0	0	0	0	0
		Time stamp					
		Class information					

Fig. 16

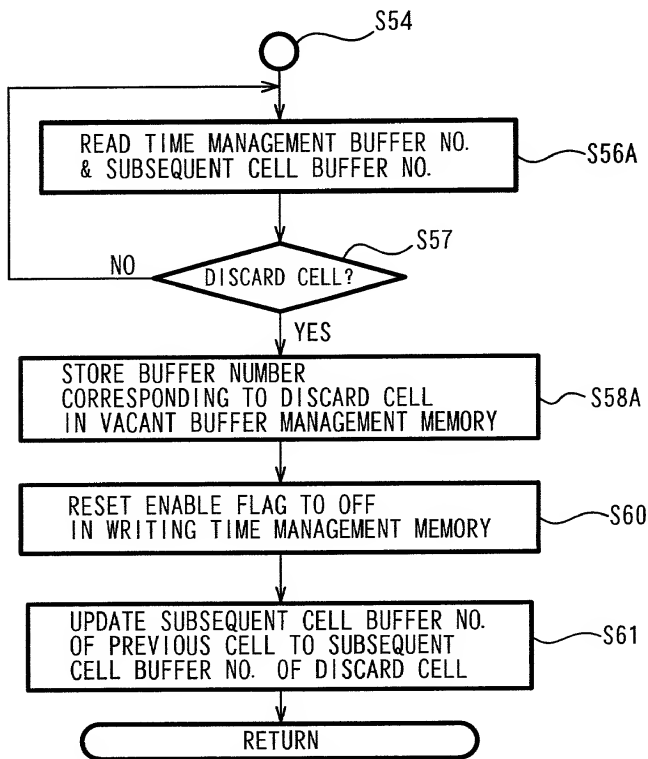


Fig. 17

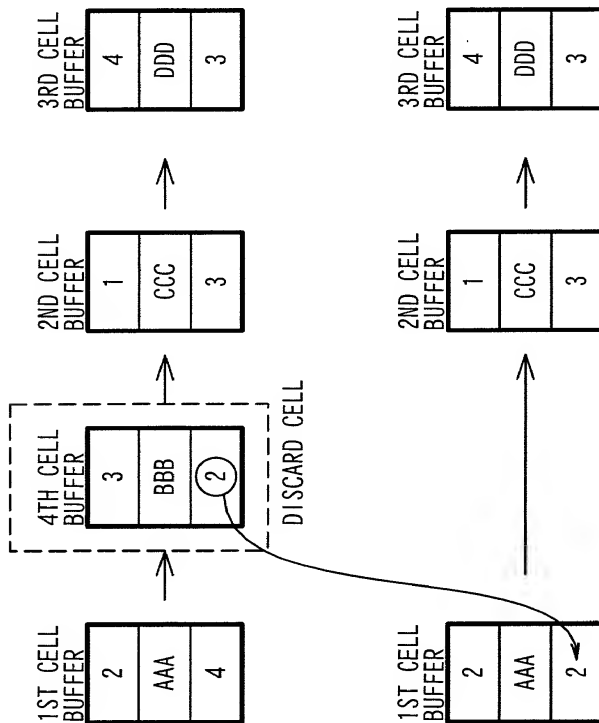


Fig. 18

